



FORM PTO 1449

U.S. DEPARTMENT OF COMMERCE
PATENT AND TRADEMARK OFFICEINFORMATION DISCLOSURE STATEMENT
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Chong H. Lee et al.CONFIRMATION NO.
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U.S. PATENT DOCUMENTS

EXAMINER INITIAL	DOCUMENT NUMBER	DATE	NAME	CLASS	SUBCLASS	FILING DATE IF APPROPRIATE
he	3,473,160	10/14/69	Wahlstrom	326	41	
he	4,486,739	12/04/84	Franaszek et al.	341	59	

U.S. PUBLICATIONS

EXAMINER INITIAL	DOCUMENT NUMBER	DATE	NAME	CLASS	SUBCLASS	FILING DATE IF APPROPRIATE
he	2001/0033188 A1	10/25/01	Aung et al.	327	141	03/13/01

OTHER DOCUMENTS (Including Author, Title, Date, Pertinent Pages, Etc.)

EXAMINER INITIAL	
he	"Protocol Independent Gigabit Backplane Transceiver Using Lucent ORT4622/ORT8850 FPSCs", Application Note, June 2000, Lucent Technologies Inc., pp. 1-10.
he	"Lucent Introduces 10Gb/s Ethernet FPGAs", Programmable Logic News and Views, Electronic Trend Publications, Inc., Vol. IX, No. 11, November 2000, pp. 7-8.
he	"ORCA ORT82G5 0.622/1.0-1.25/2.0-2.5/3.125 Gbits/s Backplane Interface FPSC", Product Brief, February 2001, Lucent Technologies Inc., pp. 1-8.
he	Virtex-II Pro Platform FPGA Handbook, UG012 (v1.0) January 31, 2002, Xilinx, Inc., pp. 1-6, 27-32, 121-126, and 162-180.
he	Rocket I/O Transceiver User Guide, UG024 (v1.2) February 25, 2002, Xilinx, Inc., pp. 1-106.

EXAMINER

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